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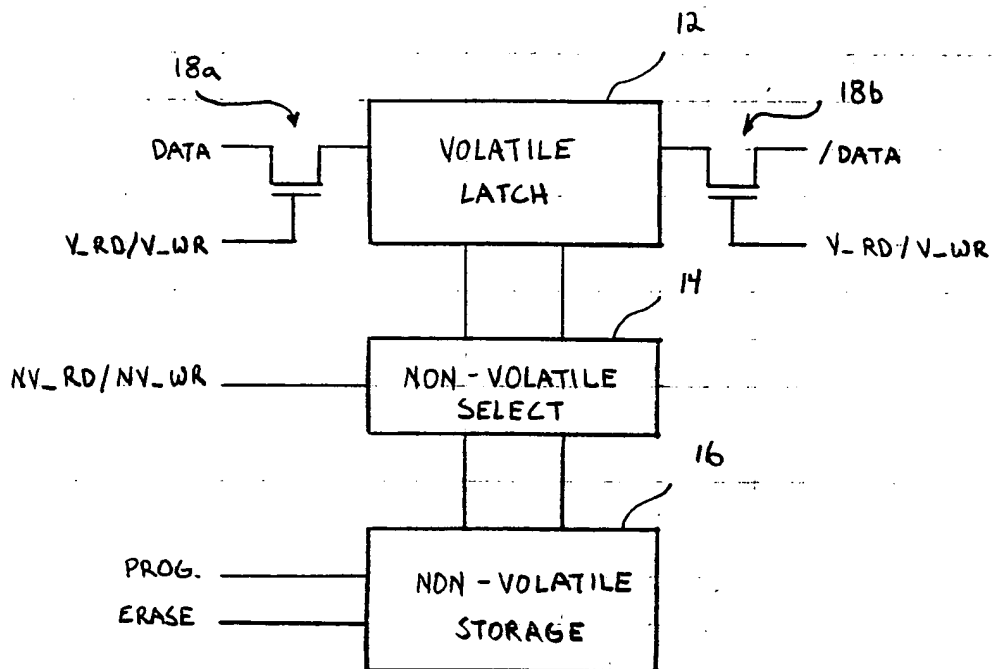


FIG. 1

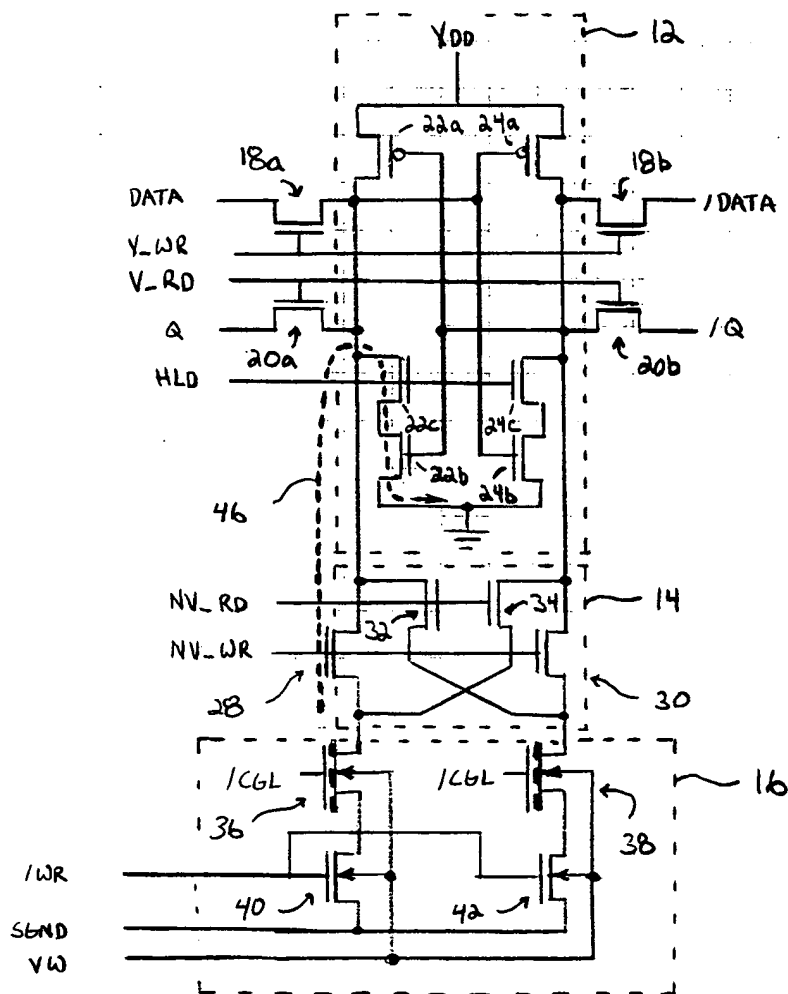


FIG. 2

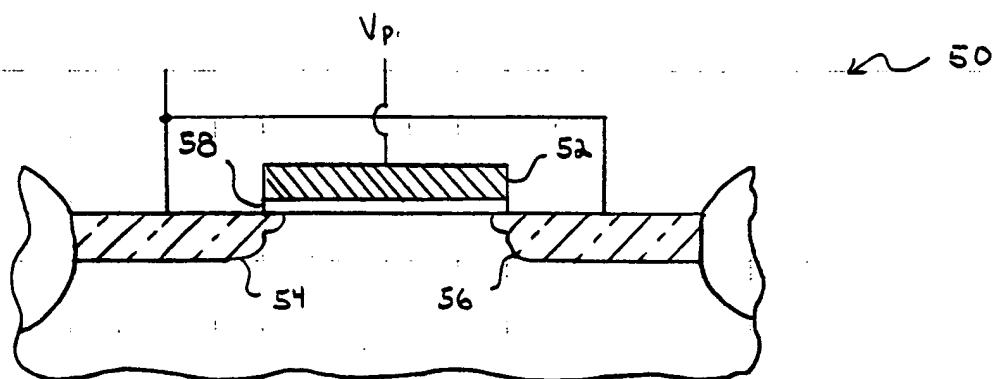


FIG. 3

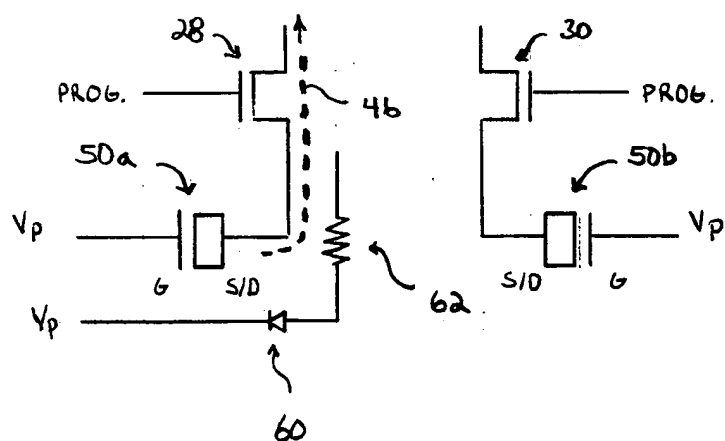


FIG. 4

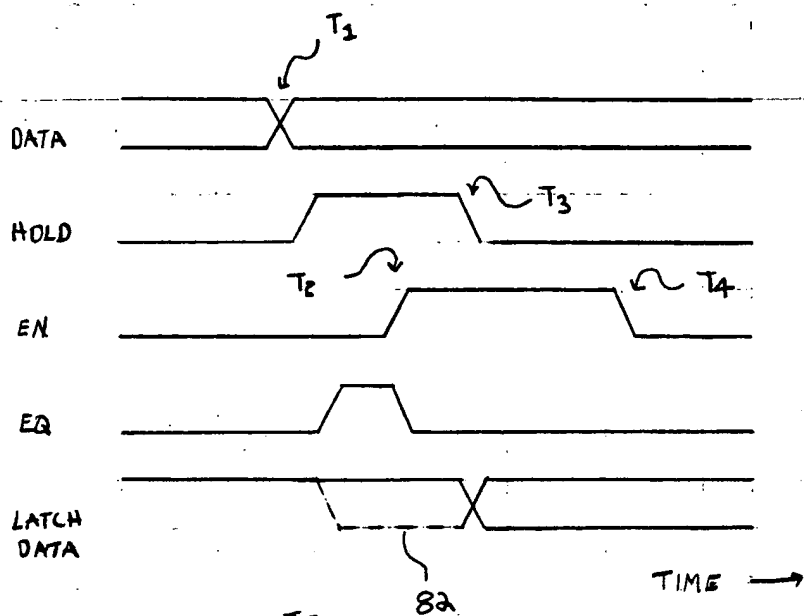


FIG. 6

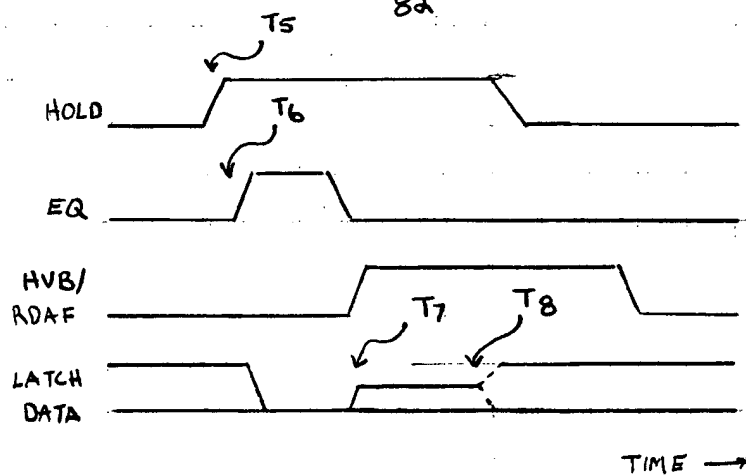


FIG. 7

	EN	HLD	NV-RD RDAF	PROG	HVB	Vp
IDLE (HOLD)	0	0	0	0	0	Low
WRITE TO LATCH FROM D-BUS	1	1≠0	0	0	0	Low
PRGM AF FROM LATCH	0	0	0	1	1	HIGH
WRITE TO LATCH FROM AF	0	0	1	0	1	Low

FIG. 8

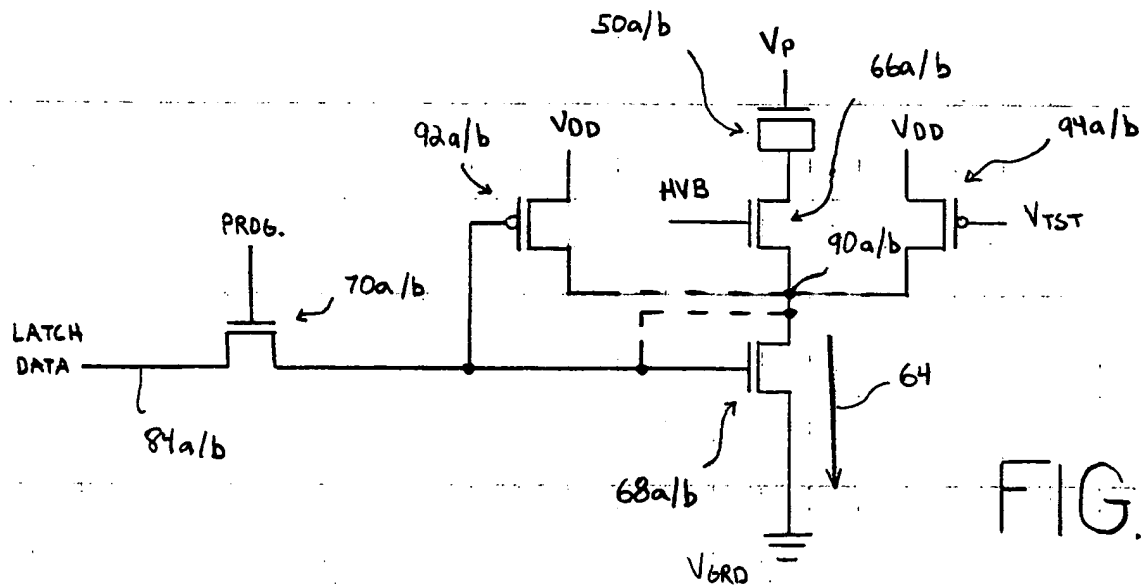


FIG. 9

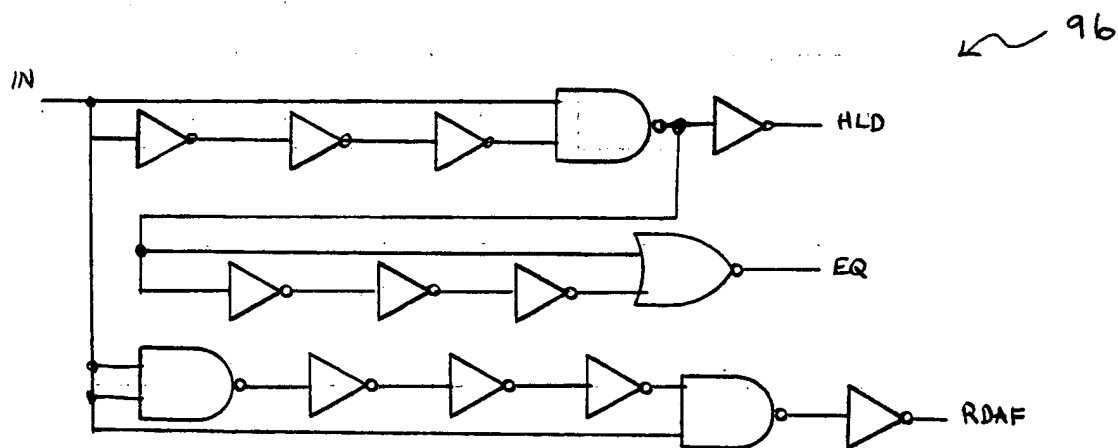


FIG. 10

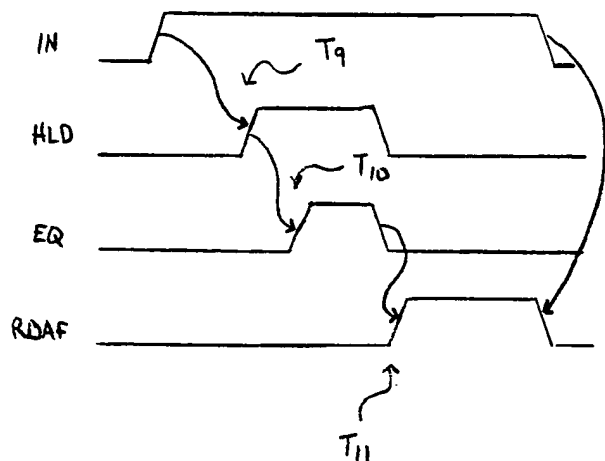


FIG. 11